



WAVEFORM GENERATION IN POWER ELECTRONIC CONVERTER USING OBJECT ORIENTED PROGRAMMING IN MATLAB

Dr. jai p agrawal, Purdue University Northwest

Jai P. Agrawal is a professor in electrical and computer engineering technology at Purdue University Northwest. He received his Ph.D. in electrical engineering from University of Illinois, Chicago, in 1991, dissertation in power electronics. He also received M.S. and B.S. degrees in electrical engineering from Indian Institute of Technology, Kanpur, India, in 1970 and 1968, respectively. His expertise includes analog and digital electronics design, power electronics, nanophotonics, and optical/wireless networking systems. He has designed several models of high frequency oscilloscopes and other electronic test and measuring instruments as an entrepreneur. He has delivered invited short courses in Penang, Malaysia and Singapore. He is also the author of a textbook in power electronics, published by Prentice-Hall, Inc. His professional career is equally divided in academia and industry. He has authored several research papers in IEEE journals and conferences. His current research is focused on renewable energy technology, smart energy grid Phasor analytical tools in ac circuit analysis.

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ABSTRACT

The paper presents the programming methodology for generating voltage and current waveforms in power electronic converters using MATLAB. Although these waveforms can also be obtained by using MULTISIM or PLEXIM simulation tools, but using MATLAB increases understanding of the circuit operation in steady state better. In this method, the power electronic converter circuit is first resolved into circuit modes (states) based on the specifications and switching strategy employed. The inductor currents and capacitor voltages are used as objects with properties and methods in each circuit state. The terminal values of each circuit state are matched with beginning values of the objects in the next circuit state. Details of classes, methods and functions will be presented in the full paper. The paper presents examples of Buck, Boost, BuckBoost, and other converter topologies. This methodology is general enough to generate waveforms in several DC-DC converters and inverters.

1. INTRODUCTION

The steady state operation of power converter is fairly complex and require clear understanding of circuit configuration which may not necessarily be constant over a period of time. Analysis and Spice simulation are two well-known methods of obtaining the time-domain waveforms of voltages and currents in different parts of the circuit. The later method is fairly accurate but does not provide enough insight in the circuit operation. Time-domain analysis method serves that purpose. Since the power converters contain a network of switches which may assume multiple configurations giving rise to piece-wise configurable circuits and transitions among them. It is extremely important to identify the circuit configurations, the sequence and the modus operandi of transitions from one configurations to another. The overall voltage and current waveforms on all components including the switches are extremely useful in obtaining the average, peak and ratings and stresses. In this paper, we present a methodical analysis of the circuit configurations, present a generalized method which can use MATLAB based method of generating waveforms of the steady state operation of converters. We employ state-variable methods for analyzing the circuit operation.

A word about its usefulness in education. This method may help in understanding the converter operation. It may be specially helpful when there is soft switching, zero voltage or zero current situations. Other areas where it can be useful when the closed form solution does not exist. This method can be used with initial estimated solution and then converge to solutions in few iterations. This methodology is more useful for senior students who have taken an introductory class in power electronics and control systems and signals.

2. POWER CONVERTER OPERATION

This paper concerns the steady-state analysis of Single Source Single Output (SISO) power converters. The power converter is a network of switches, resistor R , energy storage elements such as the inductor L , capacitor C and transformers. Switches are comprised of diodes and transistors. The source may be a dc or ac voltage or current source. The sink is a dc or ac voltage or current sink. The voltage sink is a large capacitor holding constant voltage during the steady state of operation of the converter. The current sink is a large inductor capable of holding constant current during the steady state operation of the converter. The converter operation consist of transient and steady state parts. In this paper, we will concern with the steady state operation of a power converter.

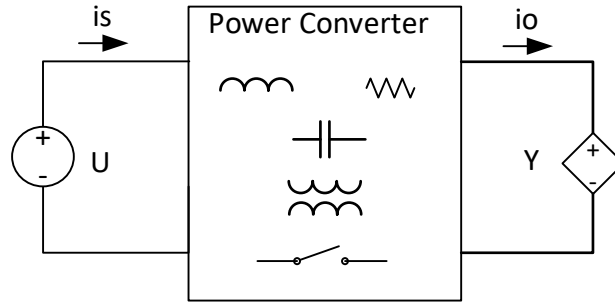


Fig. 1 SISO Power Converter

Transistors and diodes are used as unidirectional electronic switches. Transistors are turned on and off periodically by external time-driven switches and also by the reversal of current direction. The diodes are turned on when voltage across the diode terminals are forward biased, and turned off when the current flowing through them reverses or the voltage across them reverses the polarity, that is, the diodes are activated based on the currents or voltages in the circuit operation. The inductor current i_L and the capacitor voltage v_C are the two variables which usually serve as the control of diode or diode network.

If there is only one time-based switch S , the converter operation comprises of two circuit modes: one when the switch is turned on and the other when the switch is turned off. If there are two time-based switches S_1 and S_2 , the converter operation may comprise of four circuit modes. If one switch S_1 is time based and the other S_2 is based on the circuit conditions such as the current inversion or the polarity inversion of a voltage, the converter operation may consist of two, three or four circuit modes.

Each circuit mode is comprised of a network of R , L and C and an excitation U which is comprised of the sources, sinks and initial conditions on L and C 's.

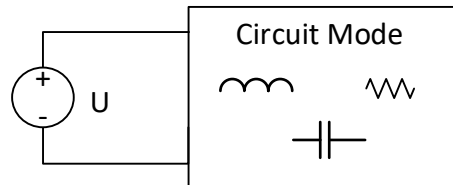


Fig. 2 A generic circuit mode topology in the steady state operation of the SISO power converter

During the operation of circuit mode, the state variables to be watched are the inductor currents and capacitor voltages. Knowledge thereof will enable us to obtain current and voltage waveforms on all other components of the converter. A circuit mode starts at t_0 and ends at t_1 or t_2 , when one of the two conditions is met first, as depicted in Fig. 3:

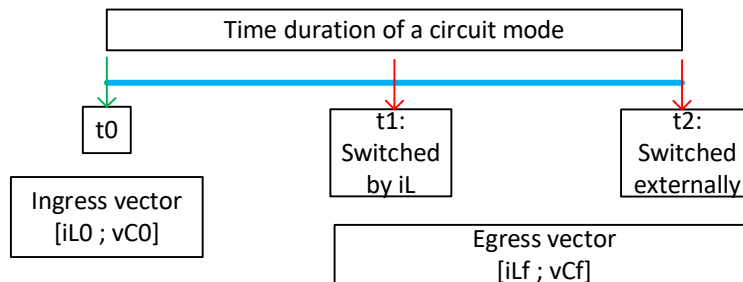


Fig. 3 Operation of a circuit mode in the power converter

The Ingress vector contains the initial state of the inductor currents and capacitor voltages at the instant t_0 . The Egress vector indicates their final states. The ending time t_1 or t_2 becomes the beginning time in the next mode in sequence of the converter operation. The Egress vector of circuit mode 1 becomes the Ingress vector of the next sequential circuit mode 2 and so on. The Egress vector of last circuit mode in the converter operation must match the Ingress vector of the circuit mode 1.

3. A CIRCUIT MODE IN THE OPERATION OF THE POWER CONVERTER

3.1 A Typical Circuit Mode

The power converter topology is a network consisting of sources, sinks, R, L, C and switches. Depending upon the state of switches, the converter assumes several circuit modes. A typical circuit mode in a dc-dc converter may look like the following:

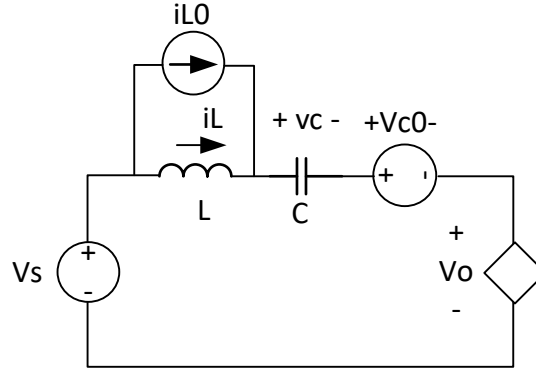


Fig. 4 A typical Circuit Mode configuration

A circuit mode has a time duration from t_0 to t_f . At any instant t in the time interval $t_0 - t_f$, the inductor current i_L and the capacitor voltage v_C are the two state variables of interest. We will enunciate them as two **objects under the class state**. Let us look at the state variable representation of the circuit at any instant of time t , given by,

$$\begin{bmatrix} \frac{di_L}{dt} \\ \frac{dv_C}{dt} \end{bmatrix} = \begin{bmatrix} 0 & -\frac{1}{L} \\ \frac{1}{C} & 0 \end{bmatrix} \begin{bmatrix} i_L \\ v_C \end{bmatrix} + \begin{bmatrix} \frac{1}{L}(V_S - V_O - V_{C0}) \\ \frac{1}{C}i_{L0} \end{bmatrix} \quad (1)$$

In the generic form, $\dot{X} = A X + B$ (2)

Where X is a vector of objects under the state class, \dot{X} is the vector of derivatives of state objects, matrix A describes the circuit network and B is the excitation vector involving the sources and the initial conditions of the state variables.

The solution of state variables is obtained in the Laplace domain from,

$$X(s) = (sI - A)^{-1} B + x_0 \quad (3)$$

x_0 is vector of the initial values of state variables. Time domain solution is obtained from the impulse response of $X(s)$,

$$\begin{bmatrix} I_L \\ V_C \end{bmatrix} = \begin{bmatrix} s & \frac{1}{L} \\ -\frac{1}{C} & s \end{bmatrix}^{-1} \begin{bmatrix} \frac{1}{sL}(V_S - V_O - V_{C0}) \\ \frac{1}{sC}i_{L0} \end{bmatrix} + \begin{bmatrix} \frac{1}{s}i_{L0} \\ \frac{1}{s}V_{C0} \end{bmatrix} \quad (4)$$

$$\begin{bmatrix} i_L \\ v_C \end{bmatrix} = \text{invlaplace} \left\{ \begin{bmatrix} s & \frac{1}{L} \\ -\frac{1}{C} & s \end{bmatrix}^{-1} \begin{bmatrix} \frac{1}{sL} (V_S - V_O - V_{C0}) \\ \frac{1}{sC} i_{L0} \end{bmatrix} \right\} + \begin{bmatrix} i_{L0} \\ v_{C0} \end{bmatrix} \quad (5)$$

We can define state vector **X** consisting of arrays of magnitudes at an array of times during the interval t_1 to t_2 as the solution of the circuit mode:

X=state(mag, time) where **X.mag**=[value 1, value 2,...value n] and **X.time**=[t0, t0+dt,t2].

The initial state of vector object **X** in a circuit mode is called the Ingress state **Ing**:

$$Ing = \begin{bmatrix} i_{L0} \\ v_{C0} \end{bmatrix} = \begin{bmatrix} \text{state}(|i_{L0}|, t_0) \\ \text{state}(|v_{C0}|, t_0) \end{bmatrix}$$

The final state of vector object **X** in the circuit mode is called the Egress state **Eg**:

$$Eg = \begin{bmatrix} i_{Lf} \\ v_{Cf} \end{bmatrix} = \begin{bmatrix} \text{state}(|i_{Lf}|, t_f) \\ \text{state}(|v_{Cf}|, t_f) \end{bmatrix}$$

The following MATLAB code is written for a dc-dc converter (Buck, Boost,...) circuit mode consisting of a single state variable, inductor current i_L :

3.2 State Class

The converter topology is a network consisting of sources, sinks, R, L, C and switches. The inductor current i_L and the capacitor voltage v_C are the two variables of interest in a converter topology. We will use them as the objects under a class **state**. The state class have two properties: magnitude and the time at which the state has that value.

```
classdef state
    properties
        mag
        time
    end
    methods
        function obj = state(val1, val2)
            if nargin > 0
                if isnumeric(val1)
                    obj.mag = val1;
                    obj.time=val2;
                else
                    error('Value must be numeric')
                end
            end
        end
    end
end
```

For example, an inductor current, when enunciated as an object under the class state, having a value of 10 mA at time of 2 milliseconds, is coded as,

`iL=state(10e-3, 2e-3).`

An object can also have an unknown value, indicated by NaN, not a number.

An object of class state can also have arrays of properties. For example, the `iL` object can have an array of 5 values at an array of 5 times,

iL=state([-1, -3, 3, -4, 2], [0, 1, 2, 3, 4]).

The state objects can be organized as an array of objects, or a matrix of objects.

3.3 Time-Based Switching of a Circuit Mode

The MATLAB function for invoking the circuit mode calculation of state variable vector in mode 1 :

X1=cmode_1b(Ing1, A, B1, Eg1, dt);

where $Ing = \begin{bmatrix} i_{L0} \\ v_{C0} \end{bmatrix} = \begin{bmatrix} state(|i_{L0}|, t_0) \\ state(|V_{C0}|, t_0) \end{bmatrix}$

$Eg = \begin{bmatrix} i_{Lf} \\ v_{Cf} \end{bmatrix} = \begin{bmatrix} state(nan, t_f) \\ state(nan, t_f) \end{bmatrix}$

The 'nan' refers to 'not a number', signifying it to be an unknown value. The t_0 and t_f are respectively the externally controlled beginning and the ending times.

3.4 Zero Current Switching of a Circuit Mode

The MATLAB function for invoking the circuit mode calculation of state variable vector in mode 1:

X1=cmode_1b(Ing1, A, B1, Eg1, dt);

where $Ing = \begin{bmatrix} i_{L0} \\ v_{C0} \end{bmatrix} = \begin{bmatrix} state(|i_{L0}|, t_0) \\ state(|V_{C0}|, t_0) \end{bmatrix}$

$Eg = \begin{bmatrix} i_{Lf} \\ v_{Cf} \end{bmatrix} = \begin{bmatrix} state(0, t_f) \\ state(nan, t_f) \end{bmatrix}$

The '0' in the magnitude of final state of the current signifies that the the mode operation ends when the inductor becomes zero for the first time. The capacitor voltage magnitude is specified as 'nan', the unknown value. The ending time t_f indicates that the circuit mode will continue up to time t_f if the zero current switching does not occur.

3.6 MATLAB Code for A Circuit Mode

```
function S = cktmode1( Ing, A, B, Eg, dt)
%Ing and Eg are respectively the initial and final state vectors in the circuit mode
%A is the state matrix for the circuit mode
%B is the excitation vector for the circuit mode that includes all sources and initial conditions on the inductor and
%capacitors.
% dt is the incremental step time in the operation of the circuit mode.
[N, N]=size(A); %size of the A matrix for the network of the circuit mode
I=eye(N); %Identity matrix for the network of the circuit mode
s=tf('s'); %Laplace variable s
Xs=inv(s*I-A)*B/s; %The solution of the state vector X during the circuit mode
t=Ing.time:dt:Eg.time; %The time array in a circuit mode
S.mag=(Ing.mag+impulse(Xs,(t-t(1))))'; %The array of magnitude of state vector S in the circuit mode
S.time=t; %The array of time of state vector S in the circuit mode
%to find the first time instant at which the inductor current becomes zero or less
if ~isnan(Eg.mag) %Check if the magnitude in the final state vector Eg is not a 'nan'
    if (Ing.mag<0) %Check if the magnitude in the initial state vector Ing is less than 0
        k=find(S.mag<=Eg.mag); %k is the array of the instants at which the vector S is less than or equal to Eg.mag
```

```

else
    k=find(S.mag>=Eg.mag);
end
S.mag=S.mag(k);    S.time=S.time(k);    %Vector S contains all values that are above zero crossing
end
end

```

4. EXAMPLES OF DC-DC CONVERTER

In this section, we shall take up the examples of steady state operation of Buck dc to dc converter in both Continuous and Discontinuous Conduction modes and a Flyback dc to dc converter in the continuous Conduction modes. Both these converter topologies are single energy storage element, therefore, simple to analyze and obtain the waveforms. The procedure is, however, generic and applies to all converters with one or more energy storage elements.

4.1 Buck Converter

The following example shows a 20V to 12V DC-DC Buck Converter operating at 200 kHz and an inductor of 24 μ H. Find the duty ratio D at which the converter must operate to deliver 20 W power to the resistive load at a voltage of 12 V.

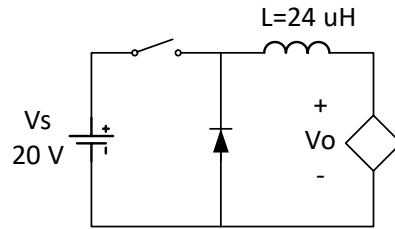


Fig. 5 Buck Converter Topology

Figure 5 shows the topology of the Buck converter. The switch is turned on and off periodically at a switching frequency f_s . The on period of the switch is $D.T_s$ where D is the duty ratio of the on-time to the switching period T_s . The output voltage is modeled by a voltage sink V_o which remains (required to be) constant in steady state for all load currents (in or out). Depending on the delivered output power, the converter operates in two modes: one in which the inductor current is never zero and the other in which it becomes zero for some duration in a period. The first is called the Continuous Conduction Mode (CCM) and the other the Discontinuous Conduction Mode (DCM). The first mode occurs at higher power P_o and the other occurs at lower power. This paper assumes that the design equations are known which are listed as below:

$$\text{Output Current: } I_o = \frac{P_o}{V_o} \quad \text{Load Resistance: } R_L = \frac{V_o^2}{P_o} \quad (6)$$

To determine if the converter would operate in CCM or in DCM, we should calculate the load resistance at the boundary between the CCM and the DCM^{[1][2]}:

$$R_{LB} = \frac{2 L f_s}{\left(1 - \frac{V_o}{V_s}\right)} \quad (7)$$

Next, we determine the duty ratio and the initial inductor current in the circuit mode 1^{[1][2]}.

The converter operates in **CCM** if $R_L \leq R_B$

$$\text{Duty ratio for the desired output voltage is: } D = \frac{V_o}{V_s} \quad (8)$$

$$\text{Peak to peak ripple current in the inductor}^{[1][2]}. \quad I_{rpp} = \frac{V_s D(1-D)}{L f_s} \quad (9)$$

$$\text{Initial value of the inductor current in the circuit mode 1: } I_{L0} = I_o - \frac{I_{rpp}}{2} \quad (10)$$

The converter operates in **DCM** if $R_L > R_B$

$$\text{Duty ratio for the desired output voltage}^{[2]}: D = \sqrt{\frac{2 P_O L f_s}{V_S(V_S - V_O)}} \quad (11)$$

$$\text{Initial value of the inductor current in the circuit mode 1: } I_{L0} = 0 \quad (12)$$

The MATLAB code consists of two parts. The Part 1 of the code uses various formulae derived from theories presented in textbooks^{[1][2][3]}. Part 2 gives the circuit mode based analysis of the steady state operation and the MATLAB codes for determining the voltages and current waveforms. Readers are expected to have the Part 1 knowledge before going over to Part 2.

MATLAB Code

```
%Buck_CCM_2019_12_9.mlx
clf, clear
Vs=20; Vo=12; Po=20; L=24e-6; fs=200e3;

%Part 1
%Calculation of the initial state of the inductor current at the beginning of circuit mode 1
Ts=1/fs; %Switching period
RL=(Vo^2)/Po %load resistance
RL = 7.2000
Io=Po/Vo %dc output current
Io = 1.6667
mode='CCM'; %assuming CCM Buck
D=Vo/Vs; %duty ratio in CCM mode
RLB=2*L*fs/(1-Vo/Vs) %boundary value of load resistance between CCM and DCM
RB = 24.0000
if(RL>RLB) mode='DCM'
    D=sqrt((Vo*Io*2*L*fs)/(Vs*(Vs-Vo))); %Duty ration in DCM
end
Irpp=Vs*D*(1-D)/(L*fs); %peak-peak ripple in the inductor current
if (mode=='DCM')
    iL0=0; %initial value of the inductor current in mode 1
else
    iL0=Io-Irpp/2
end
iL0 = 1.1667
```

The converter operation in CCM is discussed in the next section.

4.1.1 Buck in Continuous Conduction Mode (CCM) Operation (Output Power $P_O = 20$ Watts)

The Buck converter in CCM operates in two configurations: circuit mode 1 when the switch is turned on and remains on. The second circuit configuration occurs when the switch is turned off for the duration of $(1-D)T_s$. The circuit configuration and the current flow in circuit mode 1 is shown by the thick dark line in the first circuit in Fig. 6. The circuit configuration and the current flow in circuit mode 2 is shown by the thick dark line in the second circuit in Fig. 6. Circuit mode 1 transitions to circuit mode 2, which then in turn comes back to circuit mode 1 in the next period of

operation. There is only one energy storing element inductor L , therefore, the inductor current is the only state variable to watch for.

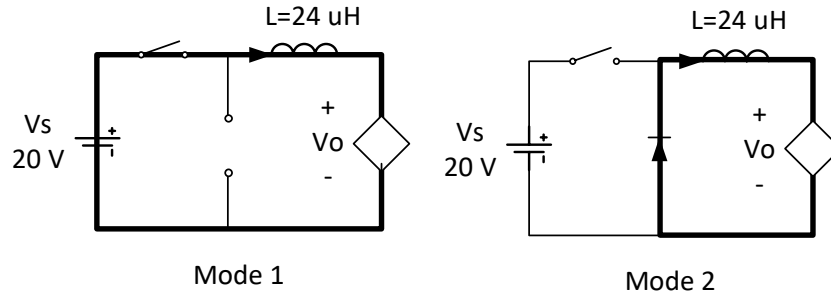


Fig. 6 Current flow in circuit modes

The simplified circuits of these modes are given below:

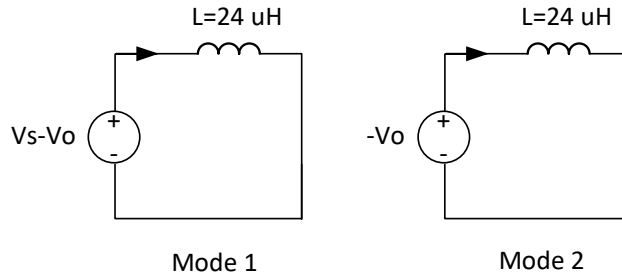


Fig. 7 Ssimplified circuits of the modes

The program declares inductor current i_L as the state variable object, calls a function **cktmode1** which uses the object in two circuit modes to determine current waveform over a switching period in steady state.

Circuit mode 1:
$$\frac{di_L}{dt} = +\frac{1}{L}(V_s - V_o) \quad (13)$$

$$I_L = \frac{1}{sL}(V_s - V_o) + i_{L0} \quad (14)$$

$$A1 = 0, \quad B1 = \frac{1}{L}(V_s - V_o), \quad \text{Ing1} = i_{L0}$$

Circuit mode 2:
$$\frac{di_L}{dt} = +\frac{1}{L}(-V_o) \quad (15)$$

$$I_L = \frac{1}{sL}(-V_o) + i_{L1} \quad (16)$$

$$A2 = 0, \quad B2 = \frac{1}{L}(-V_o), \quad \text{Ing2} = i_{L1}$$

Please note that this example has very simple circuit modes with the circuit matrix $A=0$, but the procedure enunciated, is the same for any converter with complicated circuit modes including multiple energy storage elements and non-zero circuit matrices.

%Part 2

%Circuit mode 1 operation

A1=0; B1=(1/L)*(Vs-Vo);

Ing1= state(iL0, 0);

Eg1=state(nan, D*Ts);

iL1=cktmode1(Ing1, A1, B1, Eg1, dt);

is1=iL1;

%mode 1 configuration data

%Ingress state at the beginning of circuit mode 1

%to be determined Egress state at the end of circuit mode 1

%calculate inductor current vectors in mode 1

%calculate the source current vectors in mode 1

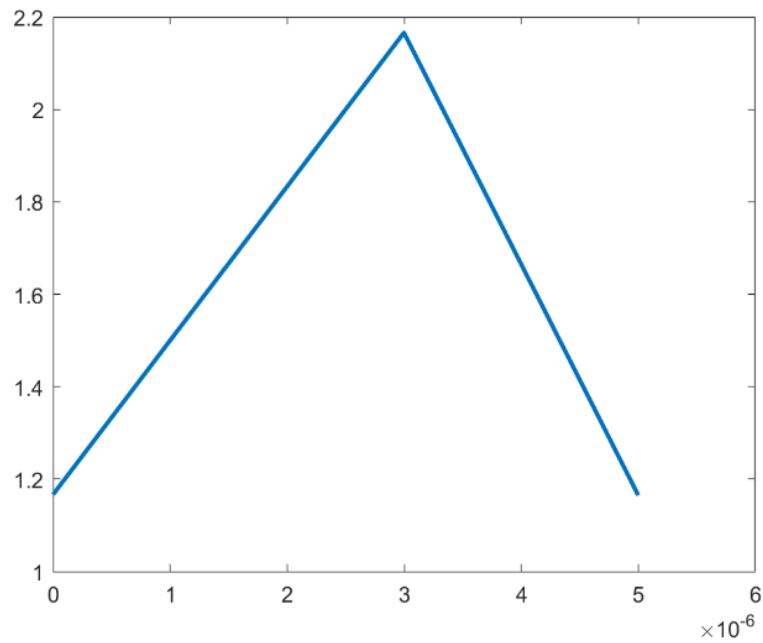
```

%Circuit mode 2 operation
A2=0; B2=(1/L)*(-Vo); % mode 2 configuration data
Ing2=state(iL1.mag(end), iL1.time(end)); %Ingress state at the beginning of circuit mode 2
Eg2=state(nan, Ts); %to be determined Egress state at the end of circuit mode 2
%Note that Eg2.magnitude must be equal to the Ing1. magnitude of the circuit mode 1, after one period Ts
iL2=cktmode1(Ing2, A2, B2, Eg2, Ts); %calculate inductor current vectors in mode 2
is2=iL2; %calculate source current vectors in mode 2
is2.mag=zeros(size(is2.mag)); %the source current is zero in mode 2
iL.mag=[iL1.mag, iL2.mag]; %inductor current in one full switching period
iL.time=[iL1.time, iL2.time];
is.mag=[is1.mag, is2.mag]; %source current in one full switching period
is.time=[is1.time, is2.time];
plot(iL.time, iL.mag, 'linewidth', 2)
plot(is.time, is.mag, 'linewidth', 2)

```

iL vs time

is vs time



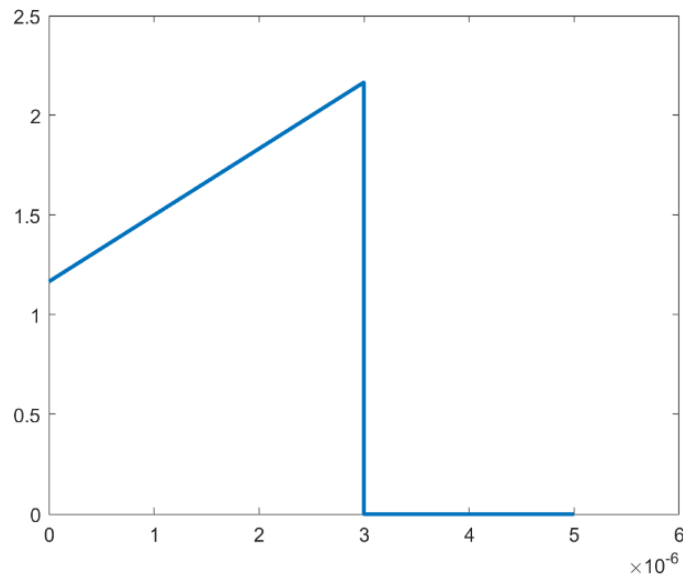


Fig. 8 a) Inductor current i_L

Fig. 8 b) Source current i_s

4.1.2 Buck Converter in Discontinuous Conduction Mode (DCM) Operation (Output Power $P_O = 5$ Watts)

The Buck converter in DCM operates in three configurations: circuit mode 1 when the switch is turned on and remains on. The second circuit configuration in circuit mode 2 occurs when the switch is turned off for the duration of $(1-D)T_s$. The third circuit configuration in circuit mode 3 occurs when the inductor current becomes zero and remains zero during the off period of the switch. The circuit configuration and the current flow in circuit mode 1 is shown by the thick dark line in the first circuit in Fig. 6. The circuit configuration and the current flow in circuit mode 2 is shown by the thick dark line in the second circuit in Fig. 6. Circuit mode 1 transitions to circuit mode 2 and then to circuit mode 3, which then in turn comes back to circuit mode 1 in the next period of operation. There is only one energy storing element inductor L , therefore, the inductor current is the only state variable to watch for.

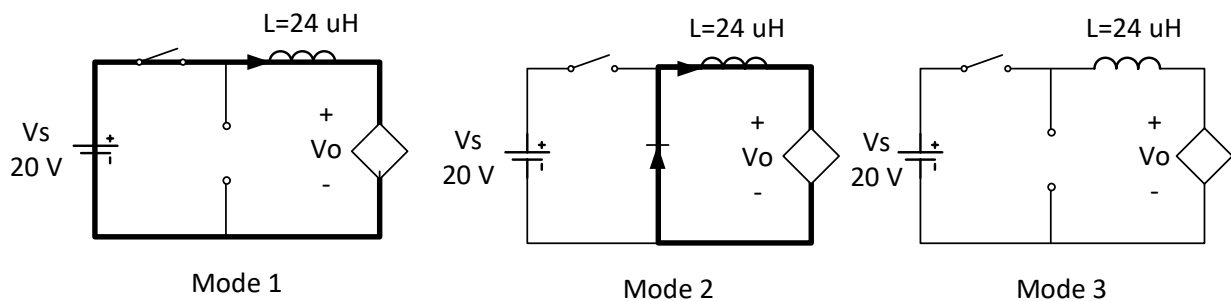


Fig. 9 Circuit modes in DCM operation of the Buck Converter

Modify the MATLAB code in part 2 by adding the following lines:

%Part 2

•
•

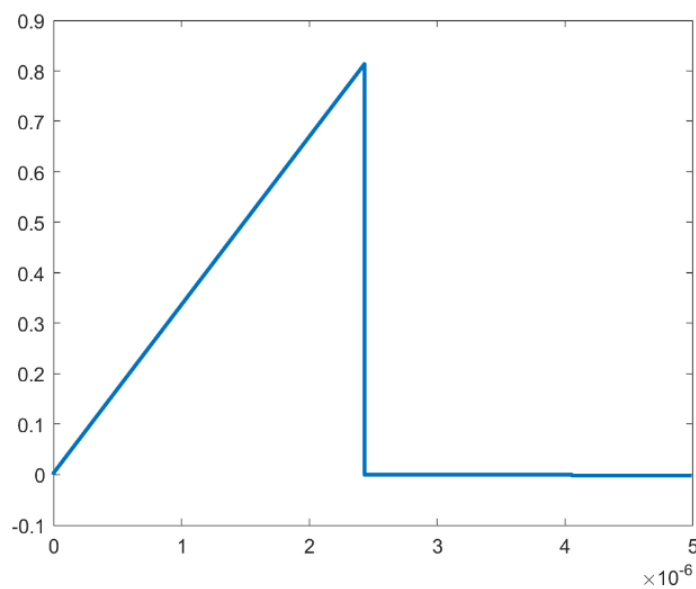
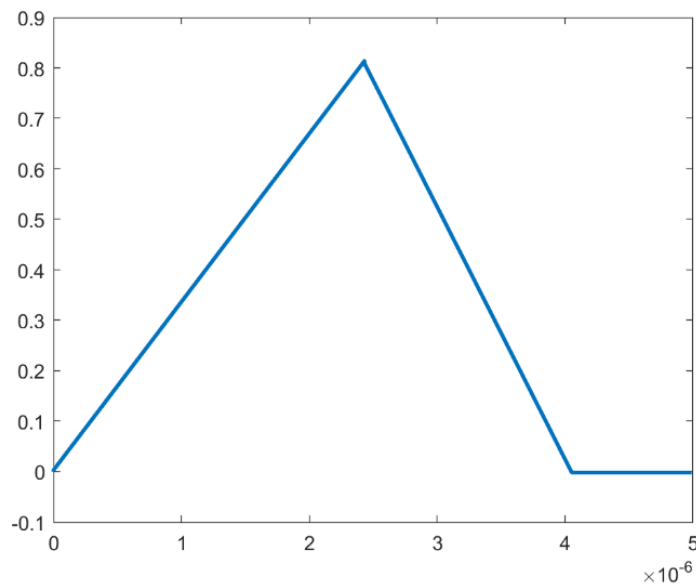
%Circuit mode 3 operation

A3=0; B3=0;

```

Ing3=state(iL2.mag(end), iL2.time(end));
Eg3=state(nan, Ts);
iL3=cktmode1(Ing3, A3, B3, Eg3, Ts);           %calculate inductor current vectors in mode 2
is3=iL3;                                         %inductor current in one full switching period
iL.mag=[iL1.mag, iL2.mag, iL3.mag];            %source current in one full switching period
iL.time=[iL1.time, iL2.time, iL3.time];
is.mag=[is1.mag, is2.mag, is3.mag];
is.time=[is1.time, is2.time, is3.time];
plot(iL.time, iL.mag, 'linewidth', 2)          %the inductor current waveform in one full switching period
plot(is.time, is.mag, 'linewidth', 2)          %the source current waveform in one full switching period

```



iL vs time

Fig. 10 a) Inductor current iL

is vs time

Fig. 10 b) Source current is

4.2 Example of Flyback DC-DC Converter in CCM

The flyback dc-dc converter is essentially a BuckBoost Converter with the inductor replaced by a transformer. The transformer provides an additional feature of electrical isolation between the input source and the output sink (load). The magnetizing inductance of the primary winding of the transformer serves as the energy storage element in the converter.

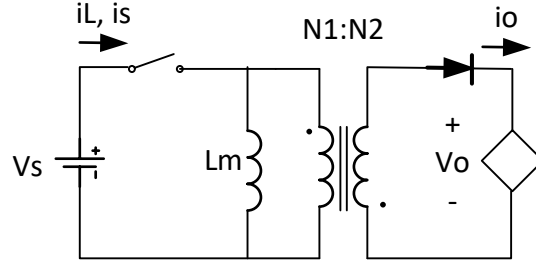


Fig. 11 Flyback Converter Topology

The following example shows a 24V to 5V DC-DC Flyback Converter operating at 40 kHz and a transformer with the primary inductance of 500 μ H and turn ratio of primary to secondary, N_1/N_2 of 3:1. Find the duty ratio D at which the converter must operate to deliver 5 W power to the resistive load at a voltage of 5V.

$$\begin{aligned} \text{DC Output Current:} \quad I_{Odc} &= \frac{P_O}{V_O} & \text{DC Source Current:} \quad I_{Sdc} &= \frac{P_O}{V_S} \\ \text{Load Resistance:} \quad R_L &= \frac{V_O^2}{P_O} & \text{DC Inductor current:} \quad I_{Lmdc} &= \frac{I_{Sdc}}{D} \end{aligned} \quad (17)$$

To determine if the converter would operate in CCM or in DCM, we should calculate the load resistance at the boundary between the CCM and the DCM^{[1] [2][3]}:

$$R_{LB} = \frac{2 L_m f_S}{(1-D)^2} \quad (18)$$

Next, we determine the duty ratio and the initial inductor current in the circuit mode 1^{[1] [2]}.

The converter operates in **CCM** if $R_L \leq R_{LB}$

$$\text{Duty ratio for the desired output voltage is:} \quad D = \frac{V_O}{V_O + V_S \frac{N_2}{N_1}} \quad (19)$$

$$\text{Peak to peak ripple current in the inductor}^{[1][2]}: \quad I_{rpp} = \frac{V_S D}{L_m f_S} \quad (20)$$

$$\text{Initial value of the inductor current in the circuit mode 1:} \quad I_{Lm0} = I_{Lmdc} - \frac{I_{rpp}}{2} \quad (21)$$

The converter operates in circuit mode 1 when the switch is closed and the magnetizing inductance L_m is energized by the voltage source V_s . The induced voltage on the secondary winding is such that it reverse biases the diode and therefore, no current in the voltage sink. In circuit mode 2 when the switch is opened and the magnetizing inductance L_m transfers energy to the transformer such that it forward biases the diode and therefore, current flows in the voltage sink.

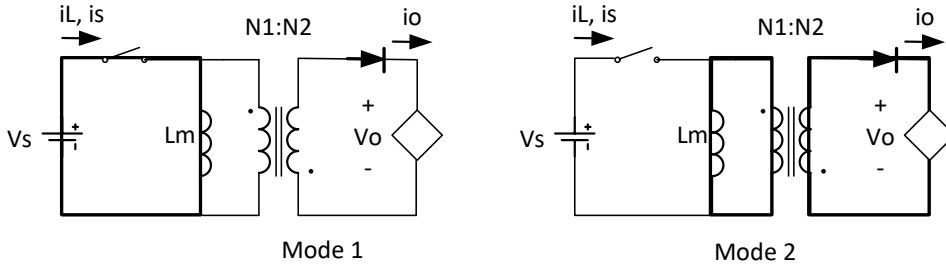


Fig. 12 Circuit modes in the steady state operation of the Flyback Converter

The simplified circuits of the modes are given below:

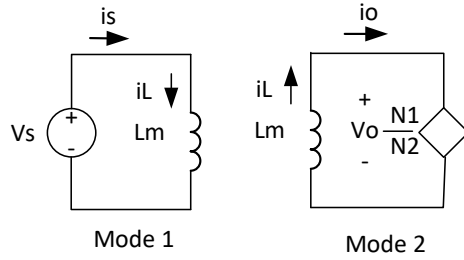


Fig. 13 Simplified circuit modes in the Flyback Converter

Circuit mode 1:

$$\frac{di_{Lm}}{dt} = \frac{1}{L_m} V_s \quad (22)$$

$$I_{Lm} = \frac{1}{sL_m} V_s + i_{Lm0} \quad (23)$$

$$A1 = 0, \quad B1 = \frac{1}{L_m} V_s, \quad \text{Ing1} = i_{Lm0}$$

Circuit mode 2:

$$\frac{di_{Lm}}{dt} = + \frac{1}{L_m} \left(-V_o \frac{N_1}{N_2} \right) \quad (24)$$

$$I_{Lm} = \frac{1}{sL_m} \left(-V_o \frac{N_1}{N_2} \right) + i_{L1} \quad (25)$$

$$A2 = 0, \quad B2 = \frac{1}{L_m} \left(-V_o \frac{N_1}{N_2} \right), \quad \text{Ing2} = i_{Lm1}$$

```
%Flyback_CCM_2019_12_13.mlx
```

```
%Part 1
```

```
Vs=24; Vo=5; Po=20; Lm=500e-6; fs=200e3; N1N2=3;
```

```
.
```

```
.
```

```
%Part 2
```

```
%Circuit mode 1 operation
```

```
A1=0; B1=(1/Lm)*Vs;
```

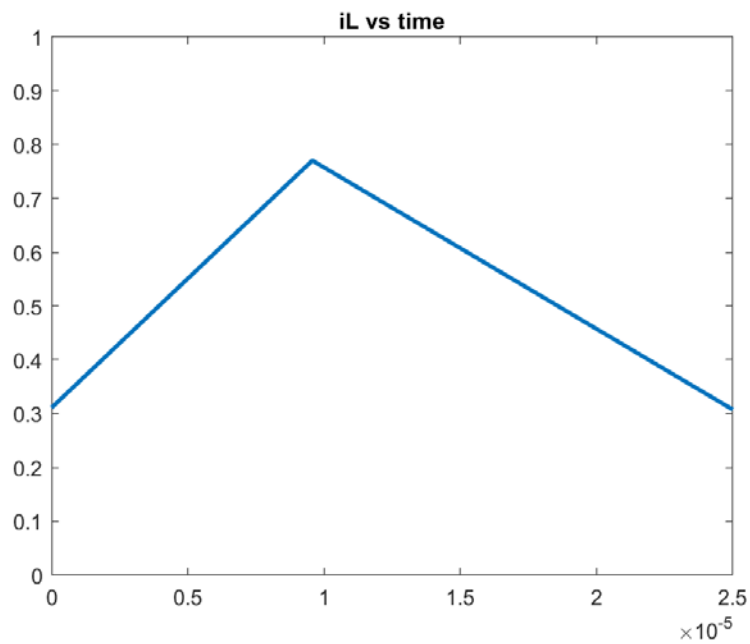
```
Ing1=state(iLm0, 0);
```

```

Eg1=state(nan, tau);
iLm1=cktmodel(Ing1, A1, B1, Eg1, dt);
is1=iLm1;
%Circuit mode 2 operation
A2=0; B2=(1/Lm)*(-Vo*N1N2);
Ing2=state(iLm1.mag(end), iLm1.time(end));
Eg2=state(nan, Ts);
iLm2=cktmodel(Ing2, A, B2, Eg2, dt);
is2=iL2;
is2.mag=zeros(size(iLm2.mag));
iLm.mag=[iLm1.mag, iLm2.mag];
iLm.time=[iLm1.time, iLm2.time];
is.mag=[is1.mag, is2.mag];
is.time=[is1.time, is2.time];
plot(iLm.time, iLm.mag, 'linewidth', 2)
axis([0, Ts, 0, 2.4]); title('iL vs time')
plot(is.time, is.mag, 'linewidth', 2)
axis([0, Ts, 0, 2.4]); title('is vs time')

```

%inductor current vector (object) in Mode 1
 %source current vector (object) in Mode 1
 %inductor current vector (object) in Mode 2
 %source current vector (object) in Mode 2
 %inductor current vector (object) in one full switching period
 %source current vector (object) in one full switching period
 %source current vector (object) in one full switching period



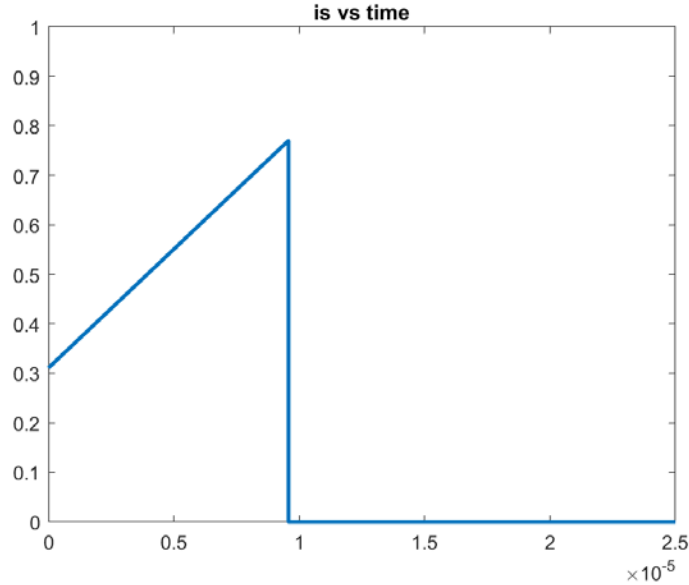


Fig. 14 a) Inductor current i_L

Fig. 14 b) Source current i_s

5 CONCLUSION

The paper presents the programming methodology for generating voltage and current waveforms in the steady-state analysis of Single Source Single Output (SISO) power converters. Use of state variable analysis and MATLAB increases understanding of the circuit operation. In this method, the power electronic converter operation is first resolved into circuit modes (states) based on the specifications and switching strategy employed. The inductor currents and capacitor voltages are used as objects with properties and methods in each circuit state.

Even though the Spice simulation is a well-known method of obtaining the time-domain waveforms of voltages and currents, it does not provide enough insight in the circuit operation. Since the power converters contain a network of switches which may assume multiple configurations giving rise to piece-wise configurable circuits and transitions among them. It is extremely important to identify the circuit configurations, the sequence and the modulus operandi of transitions from one configurations to another. The overall voltage and current waveforms on all components including the switches are extremely useful in obtaining the average, peak ratings and stresses. This paper presents a methodical analysis of the circuit configurations and proposes a generalized MATLAB based method for generating the time domain waveforms in the steady state operation of converters. We employ state variable methods for analyzing the circuit operation.

Reference

- [1] Ned. Mohan, Tore M. Undeland and William P. Robbins, "Power Electronics: Converters, Applications, and Design", 2nd ed., John Wiley & Sons, 1995.
- [2] Jai P Agrawal, "Power Electronic Systems: Theory and Design", Prentice Hall, 2001.
- [3] Daniel W. Hart, "Power Electronics", McGraw Hill, 2010.