

AC 2009-1871: APPLICATIONS OF A REAL-TIME DIGITAL SIMULATOR IN POWER-SYSTEM EDUCATION AND RESEARCH

Anurag Srivastava, Mississippi State University

Anurag K. Srivastava received his Ph.D. degree from Illinois Institute of Technology (IIT), Chicago, in 2005, M. Tech. from Institute of Technology, India in 1999 and B. Tech. in Electrical Engineering from Harcourt Butler Technological Institute, India in 1997. He is working as Assistant Research Professor at Mississippi State University since September 2005. Before that, he worked as research assistant and teaching assistant at IIT, Chicago, USA and as Senior Research Associate at Electrical Engineering Department at the Indian Institute of Technology, Kanpur, India as well as Research Fellow at Asian Institute of Technology, Bangkok, Thailand. His research interest includes engineering education and power system. Dr. Srivastava is member of ASEE, IEEE, IET, Power Engineering Society, Sigma Xi and Eta Kappa Nu. He is recipient of several awards and serves as reviewer for numerous international journals and conferences.

Noel Schulz, Mississippi State University

Noel N. Schulz received her B.S.E.E. and M.S.E.E. degrees from Virginia Polytechnic Institute and State University in 1988 and 1990, respectively. She received her Ph.D. in EE from the University of Minnesota in 1995. From July 2001 through August 2009 she was on the faculty of the ECE department at Mississippi State University and her last position was the TVA Professor of Power Systems Engineering. Starting in August, 2009 she will be the Paslay Professor in the ECE Department at Kansas State University. Noel serves on the ASEE Board of Directors as the PIC IV Chair from 2008-2010. She has also been active in the IEEE Power & Energy Society and served as Secretary for 2004-2007 and Treasurer for 2008-2009.

Applications of Real Time Digital Simulator in Power System Education and Research

Abstract

Students often perceive power engineering as an old or established field. This perception lies in the inability to show abstract concepts and new control technologies using hardware in the classroom or laboratory. New ways to effectively present the novel power system operation and control concepts are needed. Real Time Modeling and Simulation (RTMS) can be used as an approach for enhancing power engineering education and research. RTMS gives students and researchers the opportunity to witness first hand how a moderately large power system behaves and can be used to demonstrate modeling, hardware-in-the-loop (HIL) concepts, system disturbances of various types, and proper recovery actions, as well as to explain complex power system concepts. The Real Time Digital Simulator (RTDS) is an effective tool for modeling and simulation of power and control systems. RTDS hardware employs high-speed DSP (digital signal processor) chips, operating in parallel, to compute simulation results with simulation step sizes as small as two microseconds. This paper discusses projects and activities used in both teaching and research activities to provide exposure of the Real Time Digital Simulator (RTDS) for power system applications.

Introduction

The approach to teaching traditional power system topics needs to be revisited to ensure that the new graduates are equipped with the required knowledge needed in a more competitive industry. Also these new pedagogical approaches need to renew interest in power engineering to match with increasing demands for power engineering graduates in the coming years. Restructuring and deregulation of the power industry, recent blackouts and discussions about smart grids are helping to reinvigorate interest and provide increased attention to careers in power engineering. Investigating new approaches to teach power engineering courses was encouraged by the National Science Foundation (NSF) in 1997 by soliciting educational research projects targeted at developing innovative teaching tools in this area. Several grants from the funding agency were used to enhance undergraduate and graduate studies and research in energy systems¹. Modeling and simulation emerged as one of the preferred teaching approaches based on several educational research studies done in the literature².

A project aimed at developing computer exercises for use in teaching undergraduate and graduate courses in the power engineering area is described in reference³. An extensive use of the simulation technology to enhance a student's understanding of the fundamentals including practical solutions has been discussed^{4,5}. The authors have pointed out that current power engineering education has been influenced greatly by the difficulty of fully analyzing the large and complex power system, which is relatively inflexible in terms of adopting new technology developments. Power faculty are faced with a difficult choice between presenting small problems that students can grasp versus larger problems based on simulations which exhibit the true nature of the power system, but tend to overwhelm the student. The solution lies in exposing the

students to a combined set of problems using modeling and simulation tools to demonstrate and utilize advancements in science and engineering.

The Real Time Digital Simulator (RTDS)⁶ is an effective tool for modeling and simulation of power and control systems. RTDS hardware employs high-speed DSP (digital signal processor) chips, operating in parallel, to compute simulation results with simulation step sizes as small as two microseconds. The RTDS software includes a graphical user interface and detailed model library for power and control system components.

A very large system for Korean power system including 320 (3-phase) buses and 90 generators has been modeled and run in real time on the RTDS. The results of the real time electromagnetic transient simulation were validated by comparing to transient stability simulations run using PTI's PSS/E program. The differences in the results were primarily traced to the fact that the electromagnetic transient solution algorithm provides more detail and therefore greater accuracy than the transient stability algorithm⁷.

A group of faculty and students are working to develop innovative ways of teaching and research utilizing modeling and simulation power of RTDS. Most of the activities relate to integrated adaptive protective controllers, hardware in the loop techniques, power system monitoring and control as well as power electronics applications utilizing other hardware in the research laboratories. This paper summarizes the use of Real Time Modeling and Simulation (RTMS) to improve teaching and research activities contributing towards improved learning for power engineering students.

Role of RTMS in Teaching Activities

With advancements in computational power, signal processing and physical electronics, the potential of modeling and simulation has been well recognized. The AC network analyzer was one of the first power system simulator in 1960's and was improved several times utilizing better modeling techniques and science advancements⁸.

Development of a real-time digital simulation platform to explain design principles for power system controls was presented using controllers⁹. The developed platform utilizes dSPACE controller interfaced with a RTDS and successfully was used in a post-graduate university course.

At our university, the RTDS was used in several graduate courses. In graduate classes on "Power System Operations and Control" and "Power System Stability, Security and Vulnerability", students were given an opportunity to do some of their final project work on the RTDS system. There were 16 graduate students registered in "Power System Operations and Control" offered in the Fall Semester of 2006 and 25 graduate students registered in "Power System Stability, Security and Vulnerability" offered in the Fall Semester of 2007. Additionally, detailed modeling of a power system and an interface with a protection relay using the RTDS were demonstrated in the "Power System Modeling and Simulation" course. Operation of a protective relay for different types of faults and relay modeling were explained in this class using the hardware setup

as shown in figure 1. Several students completed projects demonstrating the use of the RTDS system for training and educational activities.

Some of the final group projects for the 'Power system operation and control' course, are listed here.

- i. Frequency droop characteristics of a generator: Frequency management is a one of the important goals in the control of an electric power system. The frequency droop characteristics have been studied by students through simulation. The practical and theoretical values were compared.
- ii. Power system security enhancement using FACTS (Flexible AC Transmission Systems) devices: This project relates to the impact of FACTS devices on the enhancement of power system security. An approach based on sensitivity analysis to enhance power system security was simulated. Sensitivity analysis was done for FACTS device control parameters with respect to reduction in real power flow performance index. The effectiveness of this approach was tested on a 5-bus test system and simulations were performed using the RTDS¹⁰.



Figure 1: Actual hardware setup with RTDS for real time Hardware in the Loop test

In the ‘Power system stability, security and vulnerability’ course, students have done several group projects and some projects are listed here.

- i. Dynamic analysis of voltage stability using Real-Time Digital Simulator: Using a small test system, time domain simulations were done to understand the concept of voltage stability. Here voltages at the generator buses were studied while different types of faults were introduced.
- ii. Transient stability analysis using the Real Time Digital Simulator: A five bus power system consisting of synchronous generator and dynamic loads based on classical model of a synchronous generator was developed in the RSCAD and the system transients were studied when a fault occurs on one of the transmission line. The effect of a Power system stabilizer (PSS) on the stability of the system was also analyzed. The outcome of this work was extended to find the critical clearing time which gives the maximum duration before which necessary preventive actions have to be taken for a given system. This project helped students to understand a proof of concept for the Power System Stabilizer (PSS) effect.

Feedback from students on usage of the RTDS to understand the concept of power system operation, control and stability was very positive. One of the student provided the comment, “I found the final project very helpful. It gave me a platform to apply the knowledge I gained from the lectures.” These courses were offered for the first time at Mississippi State University, so assessment data was not collected to allow comparison with previously taught classes that did not integrate the RTDS. However anecdotal results from discussions with the students demonstrated a comfort zone with advanced modeling and simulation activities that was not apparent in previous graduate students. Out of the 16 students in “Power System Operations and Control”, five chose to continue working with the RTDS as part of the research work, and 10 out of the 25 students in “Power System Stability, Security and Vulnerability” integrated the RTDS in their research work.

Role of RTMS in Research Activities

Electrical equipment manufacturers are developing advanced devices to meet the demand of higher reliability and efficiency. A simulation platform to test these devices under real system constraints before they are installed in an actual power system is extremely helpful. The RTDS allows efficient modeling and simulation of electrical power systems and provides a platform to test the equipment in loop with a modeled power system.

Most of the research activities related to the RTDS relate to closed-loop testing of physical devices such as protection and control equipment. The use of playback simulation, including the possibility of interfacing real hardware and reproducing voltage and current waveforms in real-time is a key advantage of the RTDS. The RTDS can also be used for simulation studies utilizing power electronics, renewable energy and HVDC dynamics^{11, 12, 13, 14}.

The stability of a Shipboard Power System (SPS) was analyzed using RTDS¹⁵. When a system becomes physically unstable, divergent or sustained oscillations appear in the system response after it is disturbed. In a further case study, the stability of SPS with different magnitudes of

pulsed loads switched into the system was studied. Implementation of neural network based controller for multi-machine power system using RTDS and DSP controller was presented¹⁶ to damp the power oscillations during disturbances. Damping controller design and implementation based on wide area measurements as inputs have been evaluated using digital signal processor and RTDS¹⁷.

From the utility perspective, the RTDS gives several important advantages that were previously not available allowing additional testing of new controllers and investigation of the impact of the new installations¹⁸.

At our university, a group of researchers are working on research projects mainly related to hardware in the loop, relay modeling and controller in the loop using relays, dSPACE and National Instruments controllers. Researchers are also involved in power system monitoring and control test bed development using phasor measurement unit, global positioning system (GPS), data concentrator and measurement sensor devices with RTDS. Some of the research projects are listed here.

- i. Hardware in the loop (HIL) with protective relay^{19, 20, 21, 22}: Hardware platform was setup for performing closed loop relay operation with a power system test case. At MSU's Power and Energy Research Lab (PERL), some of the relay hardware capabilities include a Schweitzer Engineering Laboratory (SEL) 351 over-current directional relay, 421 distance relay and 487 differential relay.

HIL using RTDS

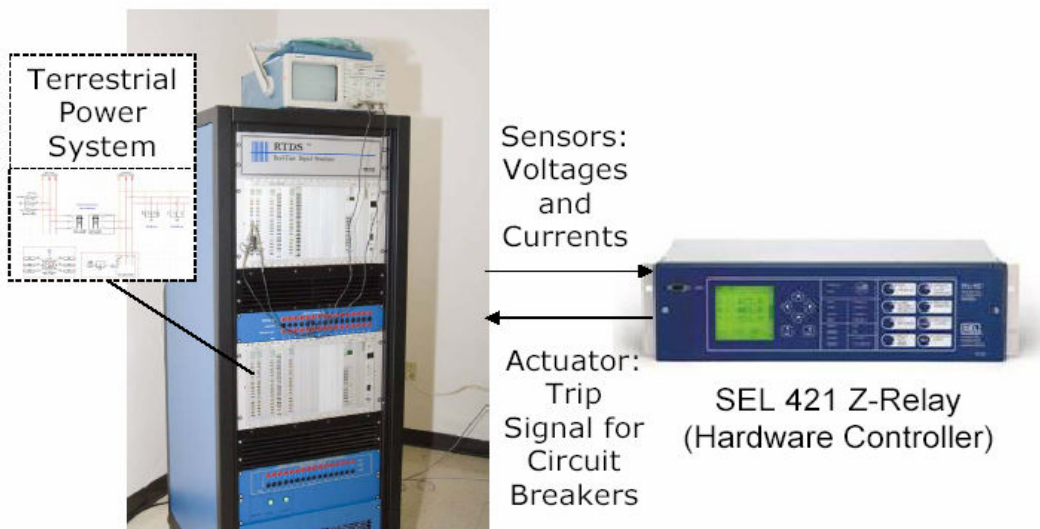


Figure 2: Signal flow between relay and RTDS for HIL setup

Figure 2 shows the data flow for HIL testing with the SEL 421 distance relay using the RTDS. A power system of 14 buses and 3 generators was built in RSCAD. The lower level signals for three phase currents, voltage and ground were given as inputs to SEL relay through the back panel of the RTDS. An output of the SEL relay to open and reclose the breaker for all three phases was given to the RTDS to operate the breaker in

the simulated power system. Figure 3 shows the voltages for different lines with single phase fault. A fault was simulated in one of the transmission lines, and operation of relay was observed. This experiment clearly demonstrated the operation of relay using the RTDS simulation and helped students to understand the protection concept.

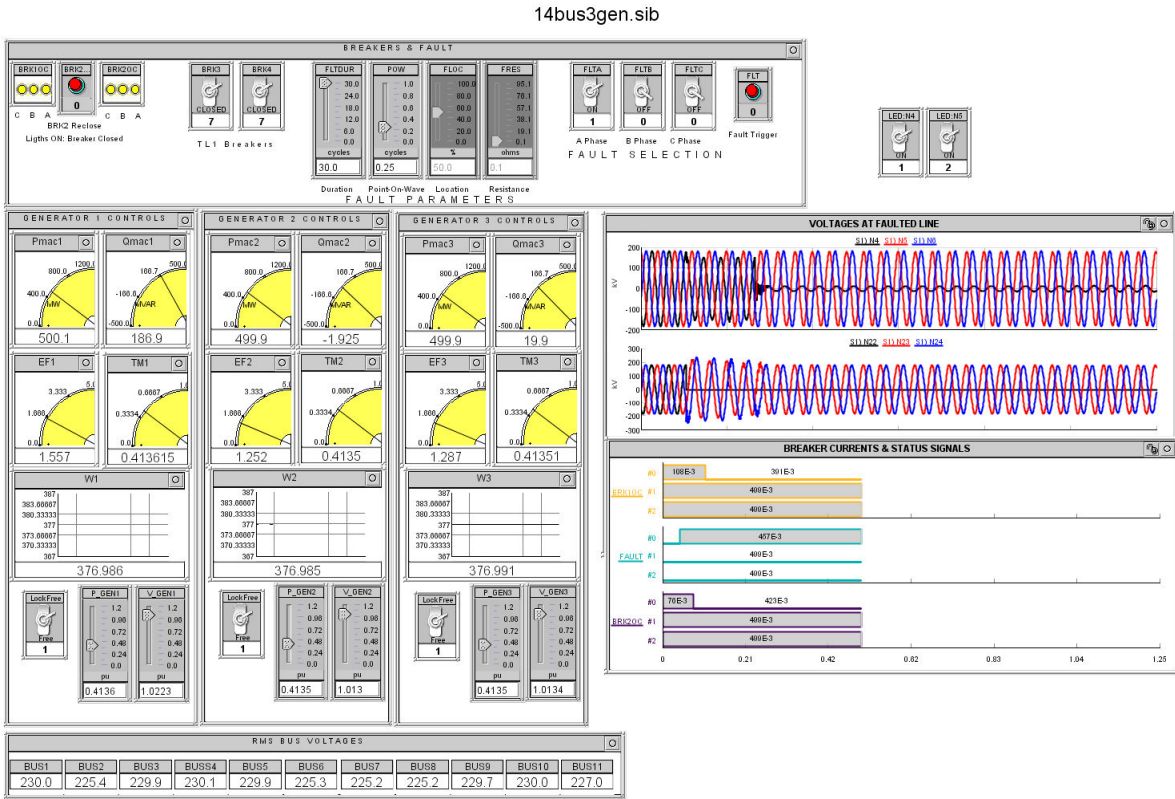


Figure 3: Voltage waveforms with fault on single phase



Figure 4. Setup for closed loop controller testing with RTDS

The hardware setup was also developed utilizing the SEL 351 and SEL 487. Software models were developed for these types of relays to allow simulating multiple relays with one or multiple relays replaced by actual hardware. The Hardware in the Loop (HIL) test provides an opportunity for understanding the behavior and validating the model of physical device. Several tests were performed for HIL and a software model in the loop test. The simulation results of the software model in the loop (SIL) test using the software relay model in RSCAD and the hardware in the loop (HIL) test using the actual relay hardware were compared in order to validate the software relay model in RSCAD.

- ii. Interfacing National Instruments and dSPACE controller with RTDS^{23, 24}: A detailed software model of an over-current relay was developed using the National Instruments (NI) LabVIEW graphical programming language.

The performance of the relay model was tested under different fault scenarios with the relay model implemented in NI and dSPACE and the power system model running in real time at RTDS. The results obtained were validated against hardware in the loop (HIL) tests conducted between the relay hardware and power system model in the RTDS. Figure 4 shows the setup for closed loop testing of the designed controller. This setup can be used to verify other control logic developed by researchers at MSU's PERL.

Other than these described research activities, researchers are actively using the RTDS for simulation of DC fault studies²⁵, an AC/DC system²⁶ and the research activities related to shipboard power systems²⁷.

Summary

Helping students visualize the engineering problems related to large complex systems is a pedagogical challenge. By using modeling and simulation tools, engineering faculty can provide educational and research activities that help students get a better feel for real life problems. Several researchers and faculty members have used Real Time Modeling and Simulation (RTMS) as a methodological approach for enhancing power engineering education and research activities. Students can obtain a deeper understanding of concepts and experience behavior of newly designed algorithms or controller actions for a moderately large power system. The RTDS can provide students a hands-on illustrative tool and help them bridge the gap between theory and practical operation of power systems. Several example power system test cases and scenarios have been successfully implemented in both the classroom and research laboratory and have been summarized here.

Acknowledgements

The authors would like to acknowledge support of the Department of Defense DURIP program for funds to purchase our RTDS (N00014-06-1-0752) and ONR funds N00014-08-1-0080 for research activities. MSU also appreciates the support of Schweitzer Engineering Laboratory (SEL) for the donation of SEL relays for educational purposes.

References

1. James A. Momoh, Arunsi U. Chuku, and Roger Austin, "Bridging the Gap for Future Educators and Researchers in Electric Energy Systems", IEEE Transactions on Power Systems, Vol. 6, No. 3, August 1991, pp 241 – 247.
2. Badrul H. Chowdhury, "Power education at the crossroads", IEEE Spectrum, October 2000, Volume: 37, Issue 10, pp 64-69.
3. Mladen Kezunovic, Ali Abur, Garng Huang, Anjan Bose, and Kevin Tomsovic, "The Role of Digital Modeling and Simulation in Power Engineering Education", IEEE Transaction on Power Systems, 2004, vol. 19, no. 1.
4. M. Kezunovic, A. Abur, A. Bose, K. Tomsovic, G. Huang and M. Venkatasubramanian, "Using Modeling, Simulation, and Digital simulators for Power Engineering Education", 3rd Int'l. Conference on Digital Power system Simulators - ICDS '99, Vasteras, Sweden, May 1999.
5. M. Kezunovic, A. Abur, and G. Huang, "MERIT 2000 - A New Concept in Power Engineering Education", Proceedings of International Conference on Energy Management and Power Delivery, EMPD, 3-5 March 1998, vol. 1, pp. 54-59.
6. R. Kuffel J. Giesbrecht T. Maguire R.P. Wierckx P. McLaren, "RTDS - A Fully Digital Power System Simulator Operating in Real Time", Proceedings of IEEE conference on Communications, Power, and Computing, WESCANEX, 15-16 May 1995, vol. 2, pp. 300-305.
7. Paul Forsyth, Rick Kuffel, Rudi Wierckx, Jin-Boo Choo, Yong-Beum Yoon, and Tae-Kyun Kim, "Comparison of Transient Stability Analysis and Large-scale Real Time Digital Simulation", Proceedings of Power Tech conference, Porto, Portugal, 9 – 13 October, 2001, vol. 4.
8. Y. Tamura, E. Dan, I. Morie, Y. Nakanishi, and S. Yokokawa, "Development of power system simulator for research and education", IEEE Transactions on Power Systems, Vol. 5, NO. 2, May 1990.
9. G. Jackson, U.D. Annakkage, A. M. Gole, D. Lowe, and M.P. McShane, "A Real-Time Platform for Teaching Power System Control Design", Proceedings of International Conference on Power Systems Transients, IPST, Montreal, Canada, June 19-23, 2005.
10. Anil K Kondabathini, Koteswar Padamati, Prashanth Duvor and Anurag K Srivastava, "Simulation of FACTS devices for Power System Security Enhancement Using RTDS", Proceedings of Power System Conference (PSC), March 11-14, 2008, Clemson, SC.
11. M. Roitman, R.B. Sollero and J.J. Oliveira, "Real Time Digital Simulation: Trends on Technology and T&D Applications", Proceedings of IEEE PES Power Systems Conference and Exposition, 10-13 Oct. 2004, vol. 3, pp. 1767- 1769.
12. W. J Giesbrecht, X. Jiang and G. Mazur, "Model development of an actual HVDC control for the RTDS simulator", Proceedings of International Conference on Energy Management and Power Delivery, EMPD , 3-5 March 1998, vol. 2, pp. 449-454.
13. Minwon Park, and In-Keun Yu, "A Novel Real-Time Simulation Technique of Photovoltaic Generation Systems Using RTDS", IEEE transactions on Energy Conversion, March 2004, vol. 19, issue 1, pp. 164- 169.
14. I. J. Van Vliet, A. M. Van Voorden, P. H. Schavemaker, G. C. Paap, and L. Van Der Sluis, "Real-Time Simulation to Study the Impact of Renewable Energy in Power Systems", Proceedings of International Conference on Future Power Systems, 18 Nov. 2005.
15. L. Qi, and S. Woodruff, "Stability Analysis and Assessment of Integrated Power Systems Using RTDS", Proceedings of IEEE Electric Ship Technologies Symposium, 25-27 July 2005, pp. 325- 332.
16. Swakshar Ray, and Ganesh K.Venayagamoorthy, "Real-Time Implementation of an Optimal Transient Neurocontroller for a GCSC", Proceedings of International Joint Conference on Neural Networks, IJCNN, 2006, pp. 2261-2266.
17. Sandhya R. Jetti, and Ganesh K. Venayagamoorthy, "Real-Time Implementation of a Dual Function Neuron based Wide Area SVC Damping Controller", Proceedings of 41st IEEE Industry Applications Society (IAS) Annual Meeting, 8-12 Oct. 2006, vol. 2, pp. 672-678.
18. Paul Forsyth, and Rick Kuffel, "Utility applications of a RTDS Simulator", Proceedings of International Power Engineering Conference, IPEC, 3-6 Dec. 2007, pp. 112-117.
19. Chenfeng Zhang, Vamsi K Vijapurapu, Anurag K Srivastava, Noel N. Schulz, Jimena Bastos and Rudi Wierckx, "Hardware-in-the-Loop Simulation of Distance Relay Using RTDS", 2007 Summer Simulation Multiconference (SummerSim'07), Jul 14, 2007 - Jul 19, 2007, San Diego, CA United States.

20. Sunil Palla, Anurag K Srivastava and Noel N. Schulz, "Hardware in the Loop Test for Relay Model Validation", Proceedings of IEEE Electric Ship Technologies Symposium (ESTS), May 21-23, 2007, Arlington, Virginia, USA.
21. Vamsi K Vijapurapu, Noel N. Schulz, Anurag K Srivastava, and Jimena Bastos, "Comparative Assessment of Differential Relay Model Performance with hardware Equipment", Grand Challenges in Modeling & Simulation, Summer Simulation Conference (SummerSim), June 16-19, 2008 Edinburgh, Scotland.
22. Ankush Saran, Padmavathy Kankanala, Anurag K. Srivastava and Noel N. Schulz, "Designing and Testing Protective Overcurrent Relay using Real Time Digital Simulation", Grand Challenges in Modeling & Simulation, Summer Simulation Conference (SummerSim), June 16-19, 2008 Edinburgh, Scotland.
23. Ankush Saran, Sunil K. Palla, Anurag K. Srivastava, and Noel N. Schulz, "Real Time Power System Simulation using RTDS and NI PXI", North American Power Symposium (NAPS), Calgary, Canada, September 28th to 30th, 2008
24. Sunil Palla, Anurag K Srivastava and Noel N. Schulz, "Modeling and validation of an overcurrent relay using LabVIEW and RTDS", Grand Challenges in Modeling & Simulation, Summer Simulation Conference (SummerSim), June 16-19, 2008 Edinburgh, Scotland.
25. Meenakshi Garg, Hymiar Hamilton, Anurag K. Srivastava, and Noel N. Schulz, "DC Fault Analysis Using Simulink and RTDS", IEEE Conference & Exhibition on Control, Communication and Automation (INDICON), Indian Institute of Technology, Kanpur, India, 11-13 December, 2008.
26. Anurag K. Srivastava, Jimena L. Bastos, Noel N. Schulz, and Herbert L. Ginn III, "AC/DC Power System Modeling and Analysis for Shipboard Applications," Proceedings of the 2007 IEEE Power Engineering General Meeting, Tampa, Florida, June 2007.
27. Noel N. Schulz, Herbert L. Ginn III, Stanislaw Gryzbowski, Anurag Srivastava and Jimena Bastos, "Ship-To-Shore Collaborations: Integrating Research Of Shipboard Power Systems Into Today's Power Engineering Research Activities," Proceedings of the 2007 Annual Conference for the American Society for Engineering Education, Honolulu, Hawaii, June 2007.