

**Computer Based Virtual Engineering Laboratory (CBVEL)
And
Engineering Technology Education**

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Abstract

This paper describes an innovative and cost-effective method of modernizing undergraduate technology and sciences education so that our graduates can be well trained with the latest technology. This will also help the technology and science programs to satisfy the TAC of ABET 2000 accreditation criteria and other accreditation needs to maintain accreditation of their programs. This paper discusses the hardware/software used in each group of this Computer Based Virtual Engineering Laboratory (CBVEL), and its use in education and research.

I. Introduction

Advanced electronics and computerization are revolutionizing today's industries and the engineering technology and science programs are under pressure to modernize their programs to meet the challenges of this changing technology or to maintain the accreditation of the programs. This requires upgrading laboratories with modern equipment and calls for increased funding and resources. But in recent years there is an increase in enrollment and decrease in resource allocation making it increasingly difficult to modernize the laboratories to provide adequate levels of laboratory and course work. The CBVEL described in this paper is designed to address these. This CBVEL will help us in providing an interdisciplinary Integrated Teaching and Learning experiences that integrates team-oriented, hands-on learning experiences throughout the engineering technology and sciences curriculum, and engages students in the design and analysis process beginning with their first year. This will modify our existing laboratories, and help us better educate and train our graduates to serve the needs of the technological and engineering community. Students can use this CBVEL along with existing software (MATLAB™ from MathWorks Inc., PSPICE™ from MicroSim Corporation, LOGIC WORKS III™ from Capilano Computing, C/C++, Visual Basic™ from Microsoft Corporation etc.) and test equipment. This CBVEL can also be accessed from remote sites using Internet.

The CBVEL consists of IBM compatible computers with appropriate software and hardware (LabVIEW, HI-Q, Virtual Bench, PXI Systems, DAQ Cards, etc.) from National Instruments (NI), and is connected to School of Engineering Technology and Sciences (SETS) network and existing equipment^{1,2}. Virtual Instrument (VI) modules for different courses and research areas are currently developed. Examples of some of these VIs are Circuit Analysis, Electronics, Communications, Digital Signal processing, and Digital Filters. These modules will be used to better train the engineering technology, sciences, and information technology graduates. Some of the research areas that will be benefited by this laboratory are Non Destructive Testing, Power Quality Analysis, Control and Robotics, Intelligent Sensors and Distributed Control, Fuzzy Logic, and Space Science. This paper is arranged as follows: Section II discusses various components used in CBVEL, and LabVIEW application areas. Section III discusses some of the VIs for circuit analysis course. Section IV deals with conclusion and discussion. Section V deals with acknowledgement and Section VI presents the bibliography.

II. Components used in CBVEL and application areas of LabVIEW

a. Components in CBVEL

The following are some of the components of CBVEL. The major components of this CBVEL are 12 PXI, 166 Mhz computers from NI and 20, 350 Mhz INTEL Pentium computers from Gateway. Besides these the PXI systems are equipped with NI components such as Function Generator Cards, Multimeter Cards, Oscilloscope Cards, Training Modules, Stepper and Servo Motion Control Cards, Virtual Bench Software, IMAQ Vision Cards, Field Point Dist. Systems, and Switches.

The PXI systems are industry standard systems with high-speed data acquisition cards. It has number slots with Windows 95 operating system. The multimeter, function generator, and oscilloscope cards are primarily for student and faculty use in different technology and science courses. Virtual Bench software is used to use these cards for different simulation and laboratory exercises. The motion control, vision, and distributed control cards are primarily for research. All these cards are installed in different ports of this PXI system. The Gateway computers are equipped with LabVIEW and HI-Q software and other software like PSPICE, Visual Basic, C/C++, MatLab, etc. The student and faculty use these computers for simulation purposes.

b. Application Areas of LabVIEW Software

The following are some of the application areas of LabVIEW^{4,5}:

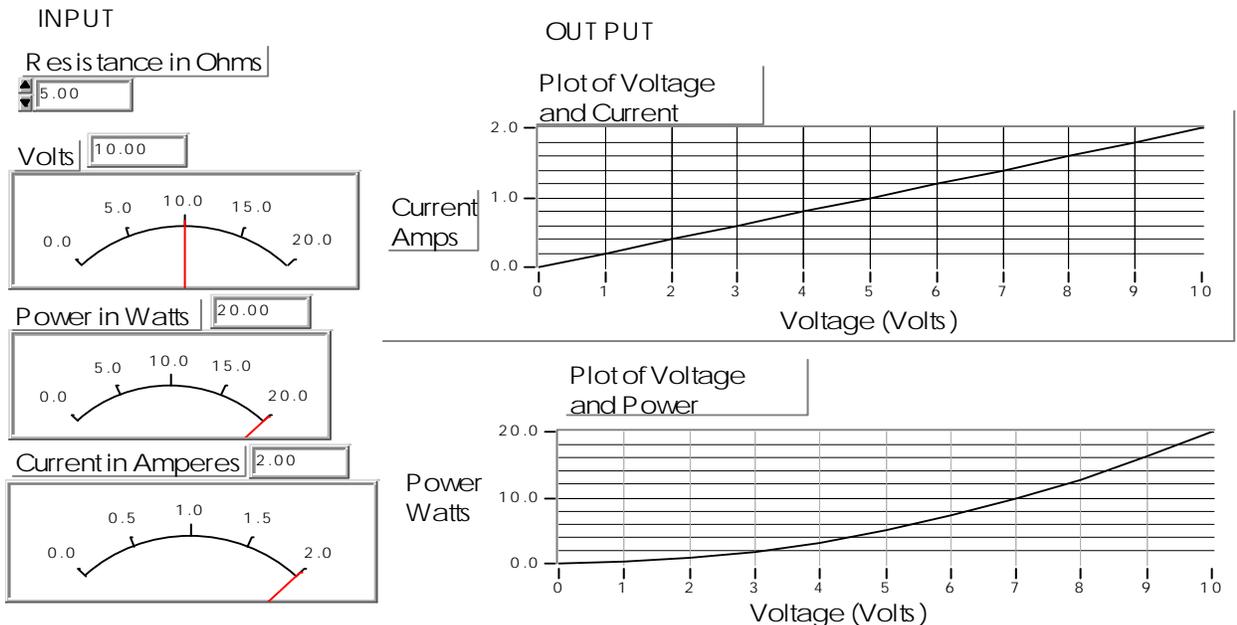
Simulation; Data Acquisition; Data Processing - built in analysis library that includes signal generation, measurement, filters, windows, curve fitting, probability and statistics, linear algebra, numerical methods; Instrument and Control; Object oriented/graphical programming; Fuzzy Logic; Genetic Algorithm; and Joint Time and Frequency Analysis. Many industries, academic institutions, and federal agencies such as DOD, DOE, and NASA use it.

III. Circuit Analysis VIs

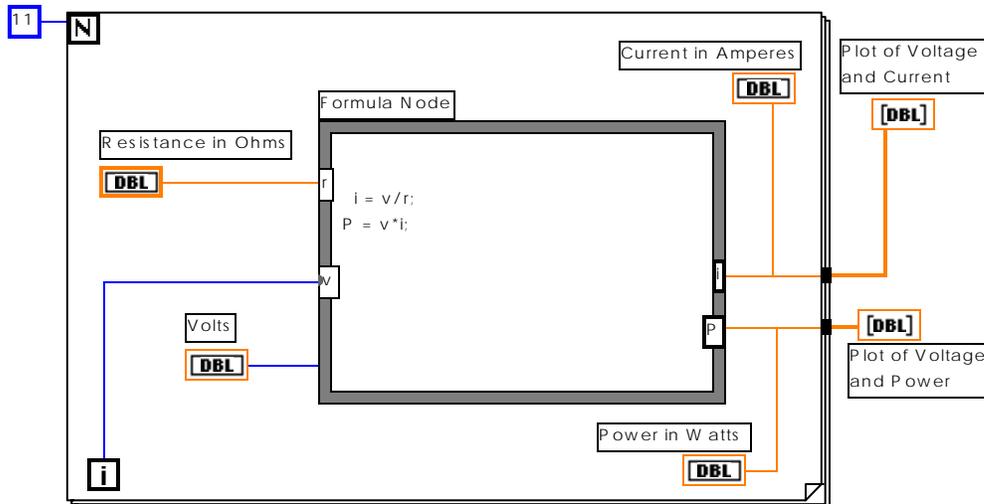
We have developed VIs for circuit analysis, electronics, communication, signal processing and filters, power and control systems, and information technology courses^{1,2}. The faculty and students use these VIs in various lecture and laboratory courses. The students build systems in breadboard and develop appropriate VI modules to test their design. They connect the design to the equipment (multimeter, function generator, oscilloscope etc.) in the PXI system module and verify their design. The circuit analysis VIs cover Ohms law, series circuit, voltage divider, parallel circuit, current divider, series-parallel circuit, capacitor charging and discharging, inductors, series and parallel RLC circuits, resonance, and frequency response. These VIs use different built in LabVIEW functions such as digital controls for input, formula node and for loop for mathematical equations, and digital indicators, graphs and charts for output. The circuit analysis VIs (Ohm's law, series-parallel circuit, and frequency response of series RLC circuit) presented below demonstrates the use of these functions.

Ohm's Law

VI Control Panel



Ohm's Law – VI diagram



Series-Parallel Circuit

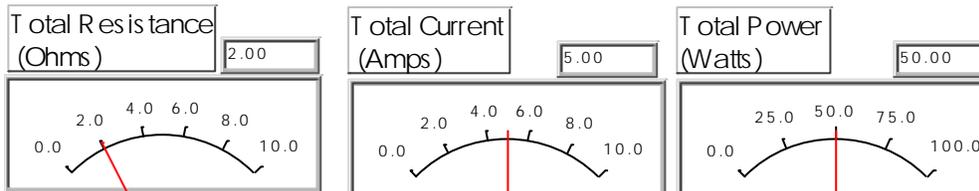
Resistor 1 is in series with the parallel combination of resistor 2 and resistor 3.

VI Control Panel

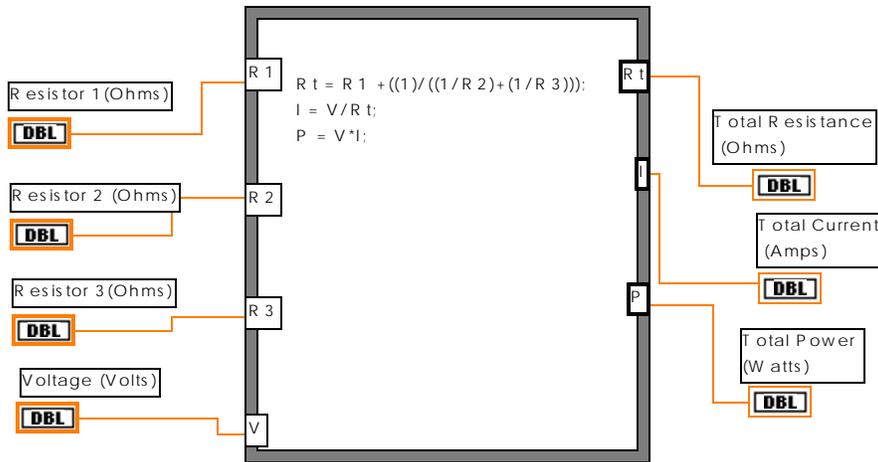
INPUTS



OUTPUTS



Series Parallel Circuit – VI diagram



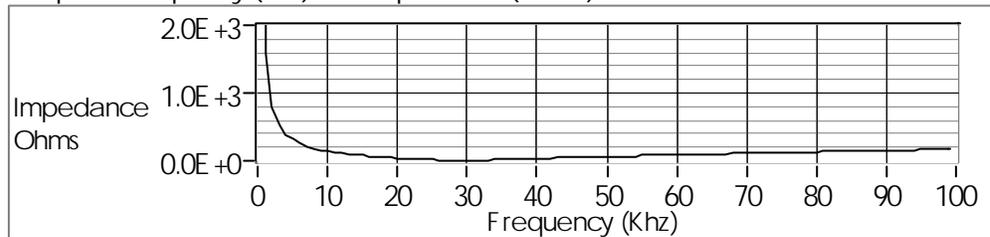
Frequency response of series RLC circuit

VI Control Panel

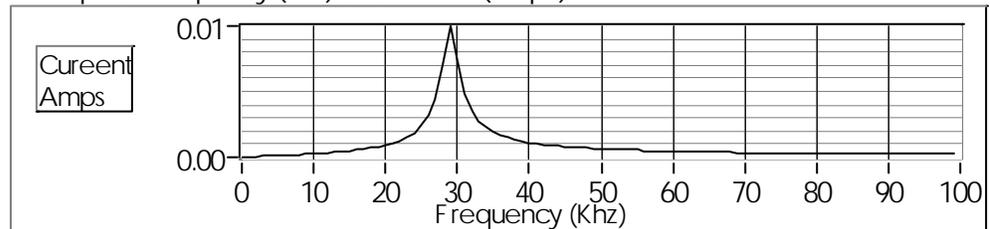
OUTPUT

INPUT	Impedance (Ohms) at Final Frequency	Resonant Frequency (Hz)	Impedance (Ohms) at Resonant Frequency
	<input type="text" value="1705.81"/>	<input type="text" value="2905.76"/>	<input type="text" value="40.00"/>

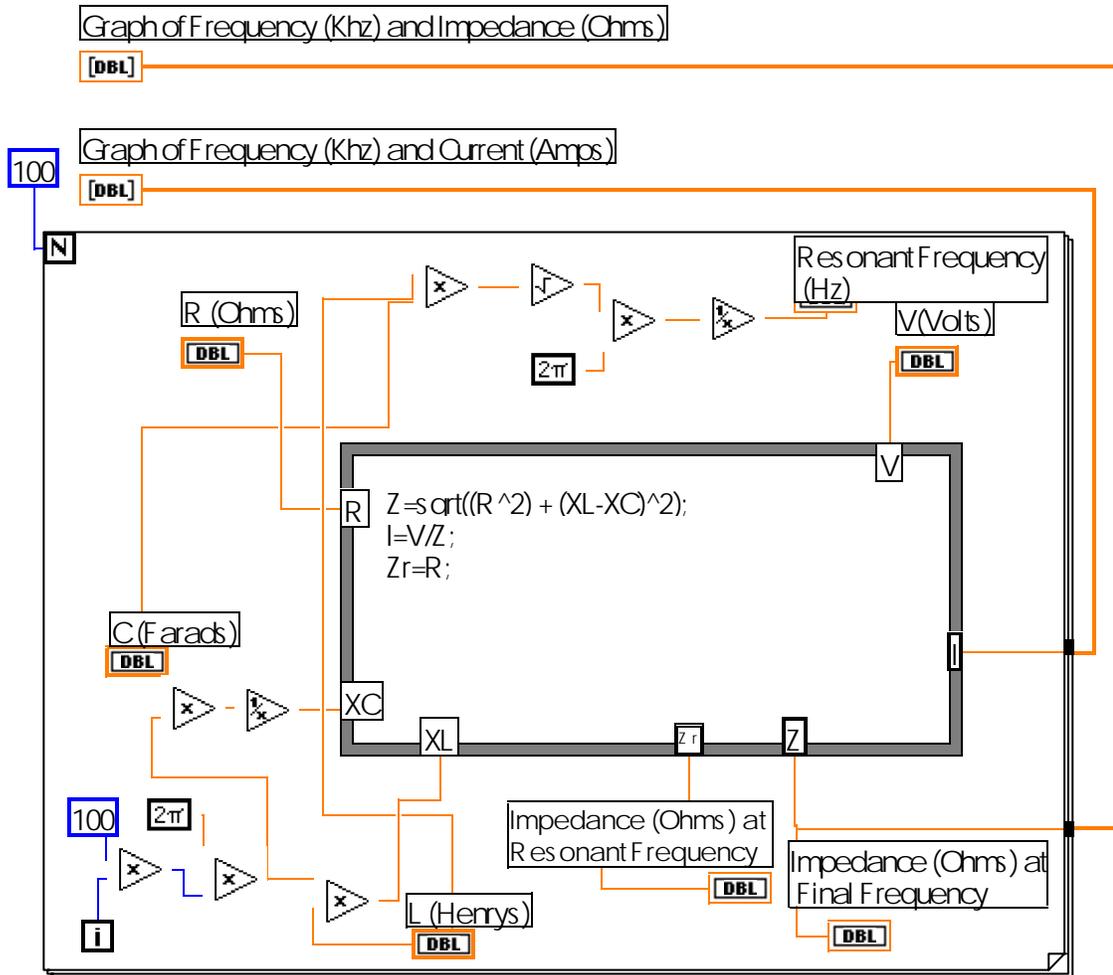
Graph of Frequency (Khz) and Impedance (Ohms)



Graph of Frequency (Khz) and Current (Amps)



Frequency response of series RLC circuit –VI diagram



IV. Conclusion and Discussion

We have developed a number of other VIs for other courses. We could present only a fraction of the VIs here because of the page limitation. The remote access component of the CBVEL is under development. The remote access feature of our CBVEL will enhance the existing distance learning courses. The Computer based Virtual Engineering Laboratory at South Carolina State University, once fully developed, will be one of its kind in the State of South Carolina. It will provide a truly modern environment in which students and faculty members can study engineering, technology, and sciences at a level of detail, and this will be possible because of the versatility of LabVIEW and other products from NI. A number of software packages are currently being used in engineering, technology, and sciences curriculum. LabVIEW has features and VIs identical to most of the features found in all these software packages.

Therefore, one can use LabVIEW only to address the needs of various courses. This will be beneficial for students and faculty and introduce standardization across the curriculum.

V. Acknowledgement

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VI. Bibliography

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